

**We are looking for you for our international team of successful
chip design and verification engineers in our
ChipGlobe doo Belgrade Design Center for 3 months**



Internship – UVM Functional Verification

ChipGlobe focuses on design and verification consulting with insourcing (at the customer site) as well as Outsourcing (ChipGlobe Design Center) working models. Our teams work worldwide with ASIC design, verification and layout teams in large semiconductor companies, focusing on automotive, telecom, security and networking designs. ChipGlobe is headquartered in Munich, Germany, with a ChipGlobe entity in Singapore and a ChipGlobe Design Center in Novi Beograd, Serbia. We are more than 70 senior experts with an experienced management team. Based on the strong experience of our employees, we carry out projects communicatively, successfully tested and sustainably.

We offer you a very good practical and current industry trend knowledge building.

- ✓ Internship includes UVM (Universal Verification Methodology) training with implementation in SystemVerilog programming language
- ✓ Intern will have adequate support from senior engineers of ChipGlobe, which will guide him during the internship. Good results and high learning ensured
- ✓ The internship lasts 3 months and a job can be offered to a successful candidate.
- ✓ Paid Internship is offered to fresh graduates or final year students of Electronics/Computer Engineering in a stable, German Chip Design/Verification Company for Semiconductors, aligned in it's business ethics and conduct with major tier 1 companies
- ✓ Stable and organised german working environment
- ✓ Internship will be held in modern business complex FOURTYONE in Novi Beograd
- ✓ Come and work for us and have a great kickstart of a Succesfull Career in Semiconductors!

Job requirements – Technical Skills

- ✓ Background in ASIC or FPGA simulation / HDL-language experience, object oriented programming, Good technical know-how

Job requirements – Soft Skills

- ✓ **Communication**
 - Able to abstract technical details
 - Good communication skills, enjoys working in an international team with some communication to Munich headquarters and EDA vendors
 - Good level of spoken and written English – must be able to understand and create technical documentation.
- ✓ **Team player**
 - Committed to the team and task
 - Helping team members, sharing knowledge
- ✓ **Self-driven and highly motivated**
 - Self-motivated, Embracing the task
 - Highlight potential issues, Organized working style
- ✓ **Solution oriented attitude, pragmatic. Good problem solving skills**
 - Following and improving processes



If you are interested in this internship, please send your CV and the reason why you would like to apply for this internship, before **31.12.2018** to: internship@chipglobe.com

Internship starts **beginning of February 2019.**