

**We are looking for you to join our international team of successful Chip Design and Verification Engineers in our ChipGlobe doo Belgrade Design Center for 3 months**



## **Internship – Functional Verification (f/m/d)**

ChipGlobe is focusing on RTL Design– and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (ChipGlobe Design Center) working models. Our teams work in a global setup with ASIC design, verification and software teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. ChipGlobe headquarters is located in Munich, Germany with a ChipGlobe Entity in Singapore and a ChipGlobe Design Center in Belgrade, Serbia and Ho Chi Minh City in Vietnam. We are more than 90 experts with an experienced management team. Based on the strong experience of our staff we manage and execute projects in a well-communicated, success-proven and sustainable way. We love what we do and we do what we love.

### **We are offering you a very good practical and latest Industry Trend Knowledge build-up**

- ✓ Paid Internship is offered to fresh graduates or final year students of Electronics/Computer Engineering in a stable, German Chip Design/Verification Company for Semiconductors, aligned in it's business ethics and conduct with major tier 1 companies.
- ✓ You will work with a Mentor Questa verification environment / Infact and work with practical training materials based on real project work. This will give you a kick start in your expertise.
- ✓ Stable and organised german working environment.
- ✓ Intern will have adequate support from senior engineers of ChipGlobe, which will guide Intern during the internship. Good results and high learning ensured.
- ✓ Internship will last 3 months, and job could be offered to a succesfull candidate.
- ✓ Internship will be held in modern business complex FOURTYONE in Novi Beograd.
- ✓ Come and work for us and have a great kickstart of a Succesfull Career in Semiconductors!

### **Job requirements – Technical Skills**

- ✓ Background in ASIC or FPGA simulation / HDL-language experience, object oriented programming, Good technical know-how
- ✓ SystemVerilog and/or UVM basic experience

### **Job requirements – Soft Skills**

- ✓ **Communication**
  - Able to abstract technical details
  - Good communication skills, enjoys working in an international team with some communication to Munich headquarters and EDA vendors
  - Good level of spoken and written English – must be able to understand and create technical documentation.
- ✓ **Team player**
  - Committed to the team and task
  - Helping team members, sharing knowledge
- ✓ **Self-driven and highly motivated**
  - Self-motivated, Embracing the task
  - Highlight potential issues, Organized working style
- ✓ **Solution oriented attitude, pragmatic. Good problem solving skills**
  - Following and improving processes

If you are interested in this internship, please send your CV and the reason why you would like to apply for this internship to:

[internship@chipglobe.com](mailto:internship@chipglobe.com) **Deadline for submission is 15 January 2020**