

We are looking for you to join our international team of  
successful Chip Design and Verification Engineers in our  
*ChipGlobe d.o.o. Belgrade Design Center*



## Junior Functional Verification Engineer (f/m/d)

ChipGlobe is focusing on RTL Design – and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (ChipGlobe Design Center) working models. Our teams work in a global setup with ASIC design, verification and software teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. ChipGlobe headquarters are located in Munich, Germany with a ChipGlobe Entity in Singapore and a ChipGlobe Design Centers in Dresden - Germany, Belgrade - Serbia and Ho Chi Minh City - Vietnam. We are more than 90 engineers with an experienced management team. Based on the huge experience of our staff we manage and execute projects in a well-communicated, success-proven and sustainable way.

We love what we do, and we do what we love.

### ChipGlobe Offering:

- ✓ Opportunity to work for stable, expanding German company with mature management, that is technically involved
- ✓ Excellent working environment, encouraging both technical and personal involvement
- ✓ Work together with a team of 90 engineers across multiple expertise domains in a global team setup
- ✓ Strategic partnerships with leading semiconductor companies
- ✓ Opportunity to travel abroad and work on client's site
- ✓ Paid private health insurance
- ✓ New and challenging innovative projects
- ✓ Office space in modern business complex GREEN HEART in Novi Beograd

### Job requirements – Technical Skills

- ✓ Good background in digital circuits or hardware design
- ✓ 0-2 years of experience in pre-silicon Verification based on digital and analog/mixed signal designs.
- ✓ Masters or Bachelor's degree in Electrical Engineering, Communications or an equivalent university program.
- ✓ Experience with HVL (Hardware verification Language) like System Verilog, Specman/e
- ✓ Functional Safety of systems/IPs - ISO 26262 experience is considered as a plus
- ✓ Applying Metric driven Verification experience in projects is key.
- ✓ Experience with creating designs using RTL (Verilog, VHDL, System Verilog) is a plus.
- ✓ Familiar with UNIX/Linux based scripting Languages like Perl, Python.
- ✓ Familiar with versioning tools (e.g., SVN/Git/ClearCase)

### Job requirements – Soft Skills

- ✓ **Communication**
  - Able to abstract technical details. Open to communicate with people on and off site. Open-minded.
  - Excellent communication skills, enjoys working in an international team across locations
  - Very good level of spoken and written English (at least B2 level).
- ✓ **Team player**
  - Committed to the team and task
  - Helping team members, sharing knowledge
- ✓ **Self-driven and highly motivated**
  - Self-motivated and self-dependent worker. Embracing the task. Highlight potential issues
  - Regular reporting. Clean and organized working style. Able to go the extra mile
- ✓ **Solution oriented attitude, pragmatic. Good problem-solving skills**
  - Following and improving processes
  - Able to follow corporate reporting standards
- ✓ **Organized, trusted, committed to a long-term employment at Chipglobe**
  - Quality awareness

If you are interested in this position, please send your CV and the reason why you would like to join Chipglobe to:

[employment@chipglobe.com](mailto:employment@chipglobe.com)