

We are looking for you to join our international team of  
successful Chip Design and Verification Engineers in our  
Chipglobe Munich GmbH or ChipGlobe doo Belgrade Design Center



## Senior RTL Design Engineer

Chipglobe is focusing on Design– and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (Chipglobe Design Center) working models. Our teams work in a global setup with ASIC design and verification teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. Chipglobe headquarters is located in Munich, Germany with a Chipglobe Entity in Singapore and a ChipGlobe Design Center in Belgrade, Serbia. We are more than 50 experts with an experienced management team and strong growth. Based on the strong experience of our staff we manage and execute projects in a well communicated, success-proven and sustainable way.

### Job requirements – Technical Skills

- ✓ Good HDL knowledge, legacy is mostly VHDL, new code SystemVerilog
  - Good design coding experience
  - Modelling with SystemVerilog
  - Data management (e.g. ClearCase)
  - Experience with IP creation and maintenance
  - Able to run linting tools (e.g. Spyglass), analyze results and fix code
  - Able to run trial Synthesis, understand basics of STA, improve code for physical
- ✓ Implementation
  - FPGA flow experience
  - Able to understand specifications and requirements (requirement driven flow)
  - Able to break down abstract target spec into design, document design work
- ✓ Support certification (safety and security)

### Job requirements – Soft Skills

- ✓ Communication
  - Able to abstract technical details
  - Open to communicate with people on and off site.
  - Excellent communication skills, enjoys working in an international team across locations
  - A very good level of spoken and written English ( at least B2 level).
  - Strong presentation and listening skills are required
- ✓ Team player
  - Committed to the team and task
  - Helping team members, sharing knowledge
- ✓ Self-driven and highly motivated
  - Self motivated
  - Embracing the task
  - Highlight potential issues
  - Regular reporting
  - Clean and organized working style
- ✓ Solution oriented attitude, pragmatic. Good problem solving skills
  - Following and improving processes
  - Able to follow corporate reporting standards
- ✓ Organized, trusted, committed to a long term employment at Chipglobe
- ✓ Ability to work under pressure and adapt to changing requirements

If you are interested in this position, please send your CV and the reason why you would like to join Chipglobe to:

[employment@chipglobe.com](mailto:employment@chipglobe.com)