

**We are looking for you to join our international team of successful Chip Design and Verification Engineers in our *ChipGlobe d.o.o. Belgrade Design Center* for 3 months**



## **Internship – Functional Verification (f/m/d)**

ChipGlobe is focusing on RTL Design – and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (ChipGlobe Design Center) working models. Our teams work in a global setup with ASIC design, verification and software teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. ChipGlobe headquarters are located in Munich, Germany with a ChipGlobe Entity in Singapore and a ChipGlobe Design Centers in Dresden - Germany, Belgrade – Serbia, Ho Chi Minh City – Vietnam and Thessaloniki - Greece. We are more than 90 engineers with an experienced management team. Based on the huge experience of our staff we manage and execute projects in a well-communicated, success-proven and sustainable way.

We love what we do, and we do what we love.

### **We are offering you a very good practical and latest Industry Trend Knowledge build-up**

- ✓ Paid Internship in a stable, German Chip Design/Verification Company for Semiconductors, aligned in its business ethics and conduct with major tier 1 companies.
- ✓ Stable and organized German working environment.
- ✓ Intern will have adequate support from senior engineers of ChipGlobe, which will guide Intern during the internship. Good results and high learning ensured.
- ✓ Internship will last 3 months, and job could be offered to a successful candidate.

### **What will you get**

- ✓ Initial Linux training
- ✓ Introduction with revision control and practice with git
- ✓ Introduction with SoC architecture
- ✓ Introduction with SoC TB, flow and methodology
- ✓ Training of System Verilog language as well as deeper introduction with UVM
- ✓ Introduction with vPlan and vManager
- ✓ Working on SoC Demo project in order to change and update the existing SoC environment
- ✓ Tests planning and development for SoC, with given Architecture specification
- ✓ Introduction with AMBA APB protocol
- ✓ Working with a Cadence verification environment to run simulation and debug written tests

### **Job requirements – Technical Skills**

- ✓ Fresh graduates or final year students of Electronics, Computer Engineering, FTN and related fields
- ✓ Knowledge of at least one programming language, preferably C
- ✓ Knowledge of object-oriented programming is a plus (Java,C++, Scala)
- ✓ Familiarity with Linux, Python and Makefile is a plus

### **Job requirements – Soft Skills**

- ✓ **Communication**
  - Able to abstract technical details
  - Good communication skills, enjoys working in an international team with some communication to Munich headquarters and EDA vendors
  - Good level of spoken and written English – must be able to understand and create technical documentation.
- ✓ **Team player**
  - Committed to the team and task
  - Helping team members, sharing knowledge
- ✓ **Self-driven and highly motivated**
  - Self-motivated, Embracing the task
  - Highlight potential issues, Organized working style
- ✓ **Solution oriented attitude, pragmatic. Good problem-solving skills**
  - Following and improving processes

**Come and work with us and have a great kick-start of a successful career in Semiconductors!**

If you are interested in this internship, please send your CV and the reason why you would like to apply for this internship to:

[internship@chipglobe.com](mailto:internship@chipglobe.com) - **Deadline for submission is 30<sup>th</sup> June 2023**