

**We are looking for you to join our international team of  
successful Chip Design and Verification Engineers in our  
ChipGlobe Belgrade Design Center**



## **Senior RTL Design Engineer (f/m/d)**

ChipGlobe is focusing on Design– and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (ChipGlobe Design Center) working models. Our teams work in a global setup with ASIC design, verification and software teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. ChipGlobe headquarters is located in Munich/Germany a ChipGlobe Design Center in Dresden/Germany, a ChipGlobe Design Center in Belgrade/Serbia and in Ho Chi Minh City/Vietnam as well as a ChipGlobe Entity in Singapore. We are more than 90 experts with an experienced management team. Based on the strong experience of our staff we manage and execute projects in a well-communicated, success-proven and sustainable way. We love what we do and we do what we love.

### **ChipGlobe Offering:**

- ✓ Opportunity to work for stable, expanding German company with mature management, that is technically involved
- ✓ Excellent working environment, encouraging both technical and personal involvement
- ✓ Work together with a team of 90+ experts across multiple expertise domains in a global team setup
- ✓ Strategic partnerships with leading semiconductor companies
- ✓ New and challenging innovative projects

### **Job requirements – Technical Skills**

- ✓ 5+ years of experience in digital design
  - Good HDL knowledge, writing VHDL and/or SystemVerilog code for Design
  - Good design coding experience
  - Modelling with SystemVerilog and/or VHDL
  - Mixed Signal basic Experience
  - Data management (e.g. ClearCase)
  - Experience with IP creation and maintenance
  - Able to run linting tools (e.g. Spyglass), analyze results and fix code
  - Able to run trial Synthesis, understand basics of STA, improve code for physical implementation
  - Able to understand specifications and requirements (requirement driven flow)
  - Able to break down abstract target spec into design, document design work
- ✓ Idea of safety and security

### **Job requirements – Soft Skills**

- ✓ Communication
  - Able to abstract technical details. Open to communicate with people on and off site.
  - Excellent communication skills, enjoys working in an international team across locations
  - A very good level of spoken and written English (at least B2 level).
  - Strong presentation and listening skills are required
- ✓ Team player
  - Committed to the team and task. Helping team members, sharing knowledge
- ✓ Self-driven and highly motivated
  - Self-motivated, Embracing the task, Highlight potential issues, Regular reporting
  - Clean and organized working style
- ✓ Solution oriented attitude, pragmatic. Good problem-solving skills
  - Following and improving processes. Able to follow corporate reporting standards
- ✓ Organized, trusted, committed to a long-term employment at Chipglobe
- ✓ Ability to work under pressure and adapt to changing requirements



If you are interested in this position, please send your CV and the reason why you would like to join  
Chipglobe to: [employment@chipglobe.com](mailto:employment@chipglobe.com)